

### **REMARKS**

This communication responds to the Final Office Action mailed on February 23, 2009.

Claims 1, 3-8, 21, 23-28, 31, 32, 34 and 36-38 are amended, claims 9 and 29 are canceled, and no claims are added; as a result, claims 1-8, 10-28, 30-32 and 34-38 are now pending in this application.

### **Specification Objections**

The specification was objected to as needing updating of the status of the related U.S. patent applications. The specification has been amended herein to update the status of the related applications, and withdrawal of this objection is respectfully requested.

### **Information Disclosure Statement**

Information Disclosure Statement PTO 1449 Forms was submitted on August 18, 2008 (twice) and January 26, 2009 (copies of which are attached herewith). The Examiner stated that the information contained therein was considered, but that office actions will not be published on a patent. The Examiner lined through the office action references in the PTO 1449 Forms indicating that they were NOT considered. It is respectfully requested that initialed copies of the PTO 1449 Forms be returned to Applicant's Representatives to indicate that the cited references have been considered by the Examiner, regardless of publication on a patent.

### **§ 112 Rejection of the Claims**

Claims 1-10, 21-32, and 34-38 were rejected under 35 U.S.C. § 112, first paragraph, as lacking adequate description or enablement. In particular, the Office states that "a step for if there is a match which is critical or essential to the practice of the invention, but not included in the claim(s) is not enabled by the disclosure." The Office further states that "added limitations [of] paring with in shared memory, the write request address with the separately transferred corresponding write data were not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was

filed.” See the Office Action, p. 3, ## 5 & 6. In view of the amended claims, these rejections are respectfully traversed.

Claims 1, 3-8, 21, 23-28, 31, 32, 34 and 36-38 have been amended to more clearly define Applicant’s claimed invention. These limitations are fully supported in the Application as originally filed on August 18, 2004. Applicant respectfully draws the Examiner’s attention to p. 5, line 24, through p. 8, line 19, of the Application. In particular, Applicant explicitly teaches that **“if the reference from the other processor does not match one of the store addresses stored in memory 16, it simply reads its corresponding data from memory. If it does, however, match one of the store addresses stored in memory 16, it waits until the data associated with that store address is written.”** See e.g. the Application, p. 6, line 27, through p. 7, line 2. Applicant further teaches that **“even though the write data and write address are sent at different times, they are received in instruction order at memory,” eliminating the need for an identifier associating an address with its associated data.** See e.g. *id.* at p. 7, line 20, through p. 8, line 6. **“Instead, the association [between the address and its associated data] is implied by the ordering.”** See *id.* at p. 7, line 24; see also original claims 9 & 29 (**“enforcing memory ordering in subsequent read and write requests to the write request address until the write data associated with the first write request is written into the shared memory.”**). Thus, no new matter has been added.

It is respectfully submitted that amended claims are supported by adequate descriptions and enablement and are reasonably conveyed to one skilled in the relevant art that Applicant, at the time the application was filed, had possession of the claimed invention. Reconsideration and allowance of claims 1-8, 10, 21-28, 30-32 and 34-38 is respectfully requested.

*Allowable Subject Matter*

Claims 11-20 were allowed.

Claims 1-10, 21-32, and 34-38 were indicated to be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. § 112 set forth in the Office Action.

Serial Number: 10/643,742

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Filing Date: August 18, 2003

Title: DECOUPLING OF WRITE ADDRESS FROM ITS ASSOCIATED WRITE DATA IN A STORE TO A SHARED MEMORY IN A MULTIPROCESSOR SYSTEM (As Amended)

CONCLUSION

It is respectfully submitted that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone the undersigned at (612) 373-6909 to facilitate prosecution of this application.

If necessary, please charge any additional fees or deficiencies, or credit any overpayments to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 25th, 2009.

CHERYL L. DANKERS

Name

Signature

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